



Technology 305: Software Development Platform for Designing & Testing Embedded Electronic Systems & Processors

The technology provides a development platform that accelerates the modeling, design, verification and test cycles for bringing complex electronic systems to market. By allowing development of embedded systems, including the applications proposed to be run on the system, before the hardware is ready, development costs and time to market are significantly reduced. The technology redefines a holistic approach to system design rather than adding steps to the design process.

The Teraptor platform includes:

- Architectural languages for modeling processors and systems
- C developer tools to automate activities required to create an embedded system:
 - Tool chain development
 - Processor verification
 - Virtual prototype development
 - Export tool for documentation

Competitive features:

- Current methodologies use a different tool or technology for solving each individual design problem such as processor design, system design, tool chain development and processor verification.
- Teraptor consolidates these into a single toolset that provides significantly higher levels of productivity due to the automation of many steps previously performed either manually or automatically, but driven by different tools each requiring its own learning and modeling work.

Development status:

A functioning prototype of the software is available now. It has been used on some internal projects conducted by Sankhya Technologies Private Ltd.

IP Status

Sankhya Technologies Private Limited owns all rights. Three international patents have been applied for that cover key innovative areas of the technology. A fourth patent application is in progress.

Competition

Many competitors exist, however none offer the complete processor and embedded systems design and test solution of Teraptor. Competitors include CoWare, ARM, Obsidian, Mentor Graphics, Synopsis, GreenHills, Diab, IAR, and Metrowerks.

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